

UNIVERSITY OF SOUTHERN CALIFORNIA

COMPUTER ENGINEERING

SCREENING EXAMINATION

EE 577a

VLSI SYSTEM DESIGN

SUGGESTED READING

Main Texts:

1. "CMOS VLSI Design: A Circuits and Systems Perspective", N. Weste and D. Harris, Addison-Wesley, Third edition, 2005.
2. "CMOS Digital Integrated Circuits", S. M. Kang and Y. Leblebici, McGraw Hill, Third Edition, 2003.

Additional Reading:

1. "Logical Effort: Designing Fast CMOS Circuits", by Ivan Sutherland, Bod Sproull, and David Harris. Morgan Kaufman.
2. "Skew-Tolerant Circuit Design", by David Harris. Morgan Kaufman.

Please be aware that these references are for guidance in BASIC knowledge. Ph.D. candidates are screened on the basis of talent, course knowledge, independent reading and experience.
